

In re application of:	: Before the Examiner:
BUFFET, et al.	:
Filed:	: Group Art Unit:
Title: <b>METHOD FOR LOCATING IDDQ DEFECTS USING MULTIPLE CONTROLLABLE COLLAPSE CHIP CONNECTIONS CURRENT MEASUREMENT ON AN AUTOMATED TESTER</b>	: Intellectual Property Law Department : International Business Machines Corp. : 1000 River Street : Internal Zip 972E : Essex Junction, VT 05452

Commissioner for Patents  
Washington, D. C. 20231

<b>CERTIFICATE OF MAILING</b>	
I, hereby, certify that on the date shown below, this correspondence is being sent by:	
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<b>FACSIMILE</b>	Date: <u>Sept. 19, 2003</u>
transmitted by facsimile to the Patent and Trademark Office.	Name <u>Deborah S. Drury</u>
	Signature <u>Deborah S. Drury</u>

Sir:

**PRELIMINARY AMENDMENT**

Kindly enter this amendment prior to initial examination. Any fees required in entering this divisional may be charged to Applicant's deposit account, 09-0456.